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Daniele LO IACONO Invent rs:

METHOD AND RELATIVE CIRCUIT FOR INCREMENTING, DECREMENTING OR

TWO'S COMPLEMENTING A BIT STRING

## Encl s dare:

7 3

For:

Patent Application: 31 pages, 38 claims

12 sheets of drawings

The suggested drawing figure to be published is <u>FIG. 19</u>. Citation Under 37 CFR 1.97 and PTO-1449
A Preliminary Amendment (with <u>2</u> replacement drawing sheets).

Assignee info:

Name: STMicroelectronics S.r.l.

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Country of Incorporation: Italy

Applicant claims priority benefit to the following foreign application(s): [X]

Country: Europe

Application No.: Filing Date: 02425538.2 August 30, 2002

The Declaration and Filing Fee are **NOT ENCLOSED**.

[X] Name, Address and Citizenship of Inventor(s) is as follows:

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[X] Associate this file with Customer No. 27975.

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PATENT TRADEMARK OFFICE

Date: August 28, 2003

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